Aport-212PG

Programmable Device Server

User Guide

Version 1.0



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1. Introduction

Aport-212PG is a programmable serial to Ethernet gateway which includes Cortex-M3 CPU, 64KB SRAM and 512KB flash. Aport-212PG is designed for users who are looking for a tiny but mighty computing platform which has FreeRTOS and IwIP pre-installed. The tool chain, Sourcery CodeBench Lite can be downloaded from mentor website or you can also use Keil from ARM. A tiny web server is also available for web-based application such as ajax. A demo web page source code is available for user's reference.

The key features are as follow:

- NXP LPC1768 ARM Cortex-M3 100MHz
- 512KB on-chip flash, 64KB SRAM
- Two configurable RS-232/422/485 serial ports
- One 10/100Mbps Ethernet ports
- One serial console port
- Support IwIP and BSD socket library
- Support tiny Web server and AJAX application
- Windows configuration utility included
- Support Telnet and serial console command
- Toolchain: Sourcery CodeBench Lite (download from www.mentor.com)

1.1 Specification

- System:
 - CPU: NXP LPC1768 Cortex-M3 100MHz
- Serial port:
 - Dert1: RS-232/422/485
 - Port2: RS-232/485
 - Baud rate: 1200 to 921600 bps
 - Flow control: None/Hardware/Xon_Xoff
 - Data bit: 5 to 8
 - Stop bit: 1 to 2
 - Protection: 15KV ESD
- Ethernet:
 - 10/100 Mbps, RJ45
 - Protection: 1500V Magnetic isolation
- Serial console port:
 - RS-232: 115200 baud rate, None flow control, 8 bits data, 1 stop bit
- **Power:** 9~40VDC power jack and terminal block
- Dimension: 108 x 78 x 25 mm (H x W x D)
- Operating Temperature: 0~70°C
- Storage Temperature: -20~85°C

1.2 Packing List

- Aport-212PG programmable device server
- Software toolchain (download from Artila FTP)
- Manager Utility (download from Artila FTP)

1.3 Optional Accessory

- CB-RJ2CON-100 (91-RJCON-100): Console Cable (RJ45 to DB9 Female, 100cm)
- DK-35A (36-DK35A-000): DIN RAIL Mounting Kit
- PWR-12V-1A (31-62100-000): 110~240VAC to 12VDC 1A Power Adaptor

2. Layout



3. Pin Assignment and Definition

3.1 Power Connector

Connecting 9~40VDC power line with the Aport-212PG terminal block or the power jack. If the power is properly supplied, the Power LED will keep solid yellow color.

3.2 Serial Port Connector

Serial Port uses a Male DB9 connector and it includes RS-232, RS-422 or RS485 signal and pin assignments are described as follow:

Pin No.	RS-232	RS-422	RS-485
1	DCD*	TXD-	-
2	RXD	TXD+	-
3	TXD	RXD+	DATA+
4	DTR*	RXD-	DATA-
5	GND	GND	GND
6	DSR*	-	-
7	RTS	-	-
8	CTS	-	-
9	-	-	-



3.3 LED Status

The LED provides the Aport-212PG operation information. The LED status is described as follow:

- **Power LED:** Power LED keeps ON if power (+9VDC to +40VDC) is correctly input to Aport-212PG.
- Ready LED: Ready LED keeps ON when Aport-212PG firmware is ready for operation.
- Link/Act LED: Link and Activity LED will turn ON when the Ethernet cable is connected. When there is network data traffic, this LED will flash.
- **RX/TX LED:** The RX/TX LED is a dual color LED that indicates the serial data traffic. The Yellow LED stands for receiving data and Green LED means transmitting data.

3.4 Factory Default Settings

IP Address: 192.168.2.127

Netmask: 255.255.255.0

Serial Port: RS-232

Baud rate: 115200

Data: No parity, 8 bits, 1 stop bit

Flow control: None

Serial Console port: RS-232

Baud rate: 115200

Data: No parity, 8 bits, 1 stop bit

Flow control: None

Web console: http://192.168.2.127:5003

Telnet console: telnet 192.168.2.127 5001

V Configure Device: 00-13-48-FF-FF					
	Basic Settings Advance	ed Options			
	Item	Value			
Opgrade	Information				
	Firmware Version	FMW V1.006			
Rehoot	Model Name	Aport-212			
Keboot	MAC	00-13-48-FF-FF-FF			
S.	Basic Settings				
Default	Device Name	Aport-212			
Settings	Lan Settings				
×.	IP Configure	Static •			
Disconnect	IP Address	192.168.2.127			
Disconnect	Netmask	255.255.255.0			
	Gateway	0.0.0.0			
	Change Passv	vord 🛛 🔧 Save to Device			

4. Artila Utility Software

4.1 Install Manager Utility Software

You can find many useful software utilities from Artila FTP. You need to install Manager Utility first in order to configure the Aport-212PG. To install the Manager Utility, please find the *ManagerUtilitysetup.exe* as shown following:



4.2 Broadcast Search

Once start Manager utility, you can click telescope icon to search the Aport-212PG in the network.



Click the device to configure its settings.

🗿 D	evices List					- 0 X			
8									
No	Device_Name	Model_Name	IP	MAC	Password	CommandPo			
1	Aport-212	Aport-212	192.168.2.127	00-13-48-FF-FF-FF	a	5001			
-									
			III			•			
four	nd device: 1								
	in actice. 1								

		Basic Settings Advan	ced Options
	ZZ	Item	Value
elect File	Upgrade	Information	
Select Eirmware file		Firmware Version	FMW V1.006
Select Finnware me	Roboot	Model Name	Aport-212
I\build\gcc\output\user_main.aff	Rebuut	MAC	00-13-48-FF-FF-FF
Browse	(5)	Basic Settings	
	Default	Device Name	Aport-212
OK Cancel	Settings	Lan Settings	
)/		IP Configure	Static -
	Disconnect	IP Address	192.168.2.127
	Disconnect	Netmask	255.255.255.0
		Gateway	0.0.00

Click the upgrade to upload the new firmware user_main.aff.

4.3 Install Software Toolchain

The toolchain, Sourcery CodeBench Lite ARM EABI Release is available at:

http://www.mentor.com/embedded-software/sourcery-tools/sourcery-codebench/editions/lite-edition/

Configure the environment to add the path of the toolchain. After installing toolchain, a new path will be added to Windows Environment i.e.:

Sourcery_CodeBench_Lite_for_ARM_EABI\bin

Restart the computer to make the new environment effective. After installation, you can test toolchain as follow:



4.4 Install Eclipse IDE

If you are interesting in using IDE to develop your program, the eclipse IDE is available at:

http://www.eclipse.org/downloads/

And choose C/C++ compiler option.

4.5 Start Your First Project

Run eclipse and select a workspace: **BSDrIs\Examples**. You can find the path of the example program on Artila FTP with path: **BSDrIs\Examples**.

elect a works	pace		
Eclipse SDK Choose a we	stores your projects in a folder called prkspace folder to use for this sessior	a workspace. 1.	
Workspace:	F:\tmp\BSDrls\Examples	•	Browse
Use this a	s the default and do not ask again		
		ОК	Cancel

Choose C/C++ in the Workbench.



Modify the make file to compile the program as follow:

Make Target	
Same as the t	arget name
Make target: all	9
Build Command	1
🗏 Use builder se	ettings
Build command	: cs-make
Build Settings	
🛛 Stop on first 🛛	build error
🗷 Run all projec	t builders

Use make file to build target.

Projec III III IIII IIIIIIIIIIIIIIIIIIIIIII	11 · 교 전 쇼 - 쇼 · 위 · 위 · 단 수 · 수 ·	: 2 • 2 • 3 • 3 • 3 • 3 • 3	• • • • • • • • • • • • •	II II III C/C++ & ^J
4	Projec 22 Programmable Program	<pre>user main.c struct netconn "conn, "n err_t err; struct netbuf "buf; void "data; ui6_t len; sprint(dbg_msg, "E01] %s console_write(dbg_msg); >roblems @ Tasks @ Console % consoles to display at this time.</pre>	Cutline Make Target Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make Make	(e)

Once project is built, you will find the target execution file *user_main.aff* is generated and available at: *E01_echoServer_netconn_api\build\gcc\output*

😂 C/C++ - Programi	mable212/E01_echoServer_netconn_api/src/ap	p/user_m	nain.c - Eclipse SDK 🛛 📼 💷	x
File Edit Source F	Refactor Navigate Search Run Project Win	dow Hel	р	
	₫ - 83 - ፪ - 63 - %, - 19 - 19 - 0 - 0. 	• 💩 🕫	• J I I E C/C++	- <mark>&</mark> >>>
 Projec 20 Projec 20 Programmable F01_echoSe build gcc outpu Make keil src app E02_echoSe E03_echoSe E03_echoSe E04_S2Esen E05_web&s 	<pre>@ User_main.c ≥ { struct netconn *conn, *newconn; err.t err; struct netbuf *buf; void *data; ul6_t len; sprintf(dbg_msg, "E01 %s(%s %s)\r\n", A console_write(dbg_msg); Tm Problems @ Tasks © Console ≥ □ Propert CDT Build Console [Programmable212] add block number change to 375 for larger space bodyblocknum 615 header num 5 headerEllockNum 5 headerEllockNum 5 headerLeni640, padLen=384 rm *.tmp **** Build Finished *****</pre>	ppVer tites = Pro	Coutine • Make Targ Outline	• • • • • • • • • • • • • • • • • • •
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